

Figure 1

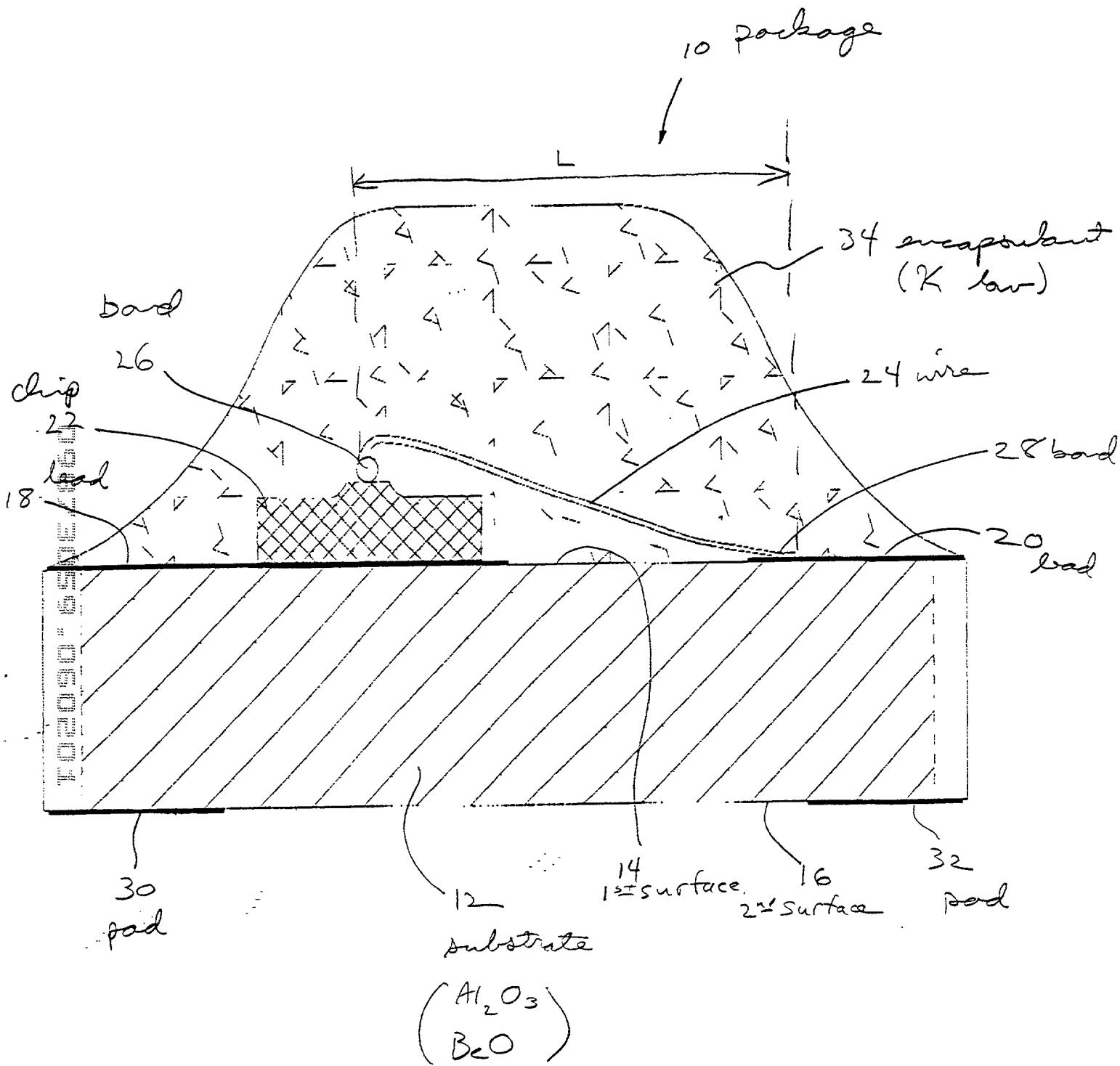


Fig. 2 TOP

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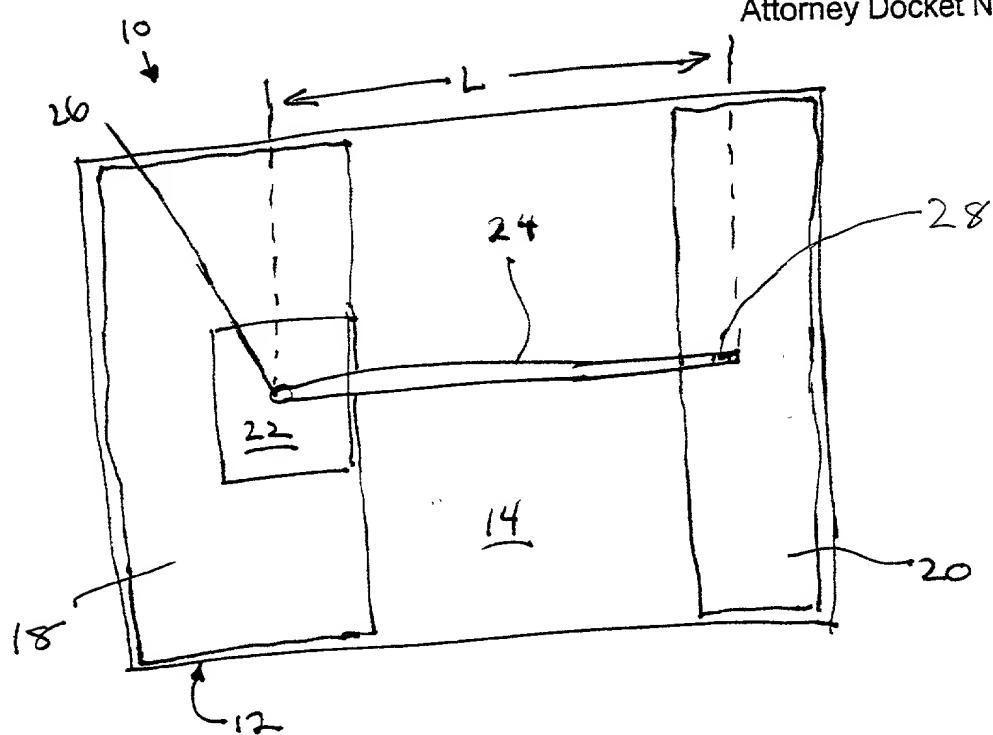


Fig. 3 bottom

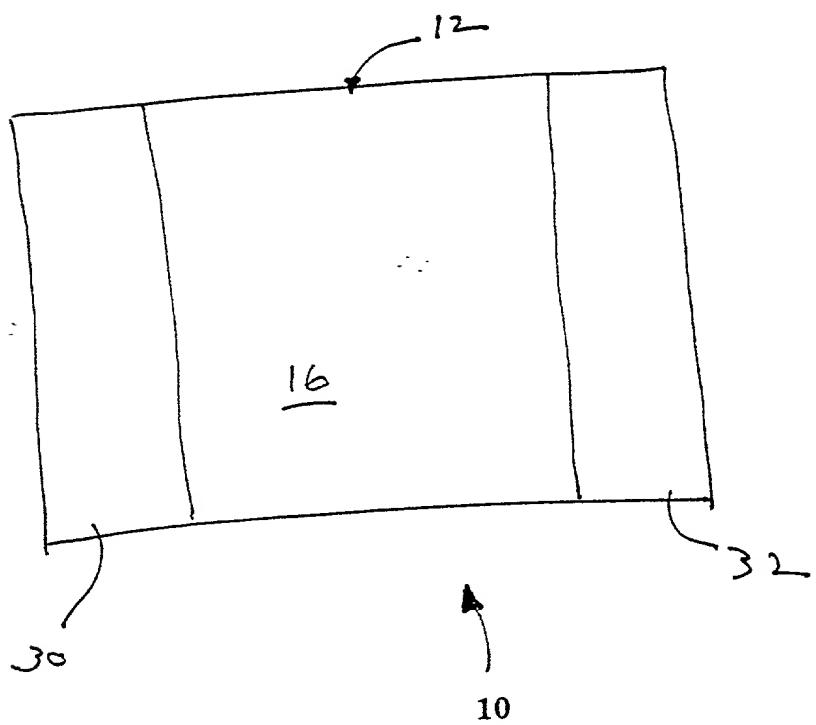


Fig. 4

Top perspective view showing
multiple chip (22) implementation

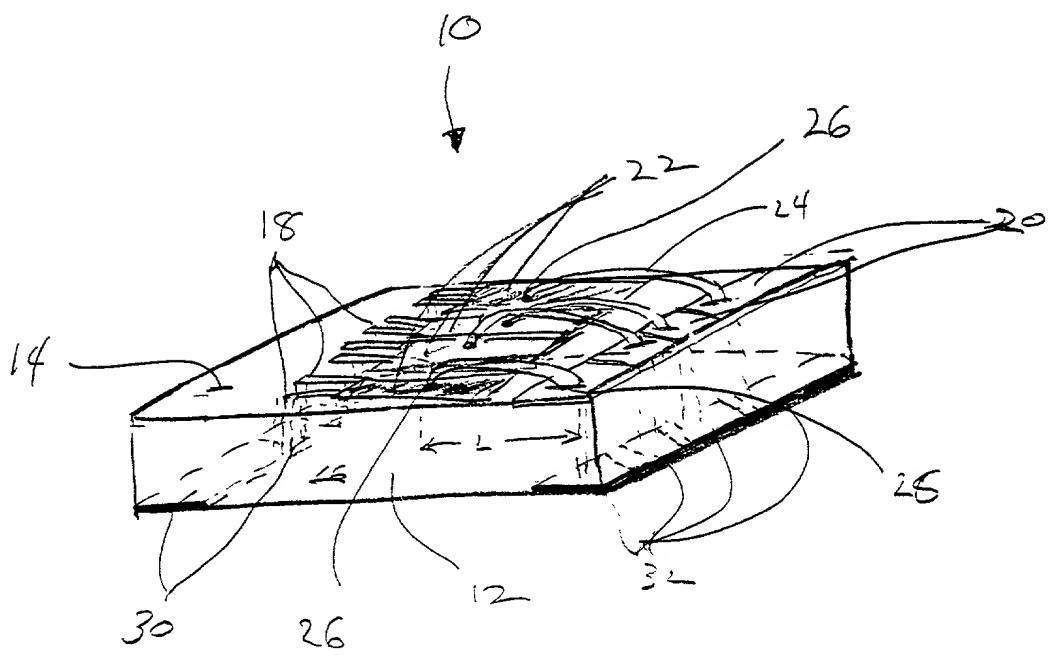


FIGURE 5

